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AUG 0 5 2005

PATENT (5298-04700/PM00028)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of:

Gilboa et al.

Scrial No. 09/846,119

Filed: April 30, 2001

For:

METHOD OF MAKING A

PLANARIZED SEMICONDUCTOR

STRUCTURE

Group Art Unit: 1763

Examiner: Goudreau, G.

Atty. Dkt. No. 5298-04700

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, V/2231 3 on the date indicated

Apgust 5, 2005

Date

## AMENDMENT; RESPONSE TO NOTICE OF DRAWING INCONSISTENCY

Box: Issue Fee

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313

Dear Sir/Madam:

This paper is submitted in response to the Notice of Drawing Inconsistency mailed July 5, 2005.

Amendments to the Drawings begin on page 1 of this paper.

Remarks begin on page 2 of this paper.